



# PRODUCT SPECIFICATIONS Module No.: NTD-3.5S320240R100D

TFT(Thin-Film-Transistor) Color Liquid Crystal Display Module

- 3.5inch Diagonal
- 320xRGBx240 resolution
- 24bit RGB interface
- LED Blacklight (500cd/m²)
- 16.7 M colors Normally Black
- Wide Viewing Angles
- RoHS Compliant

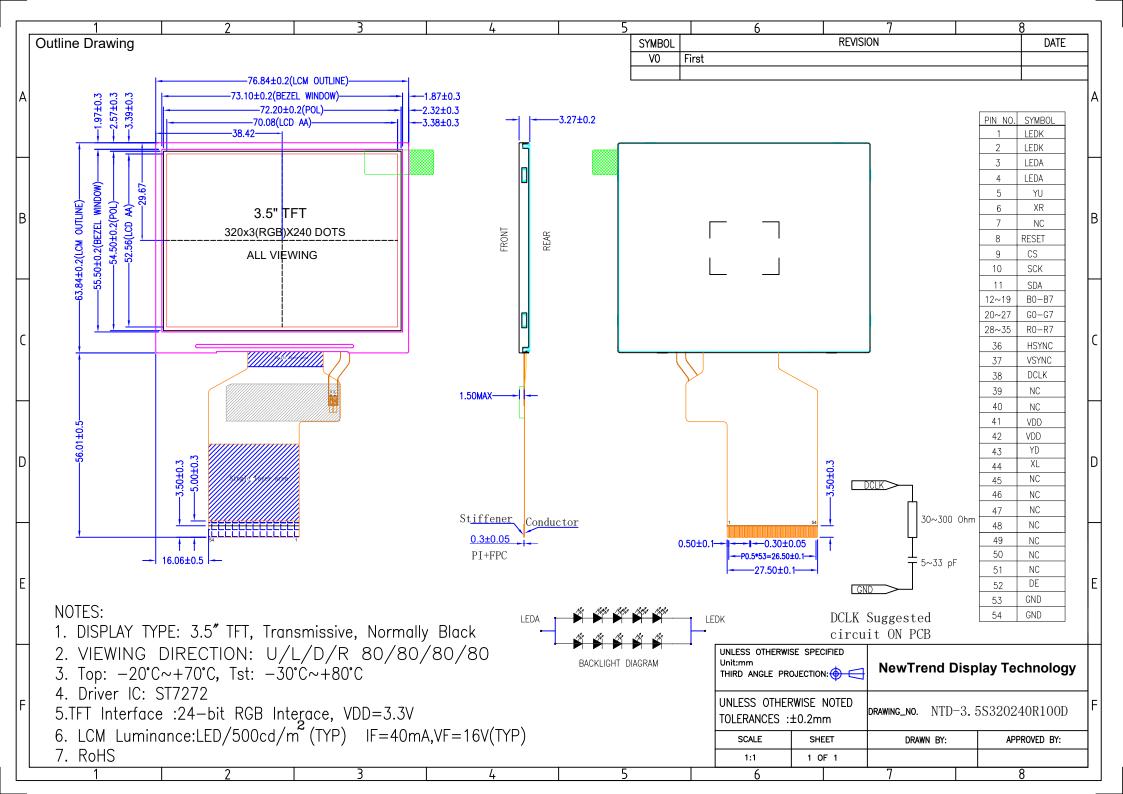
#### For Customer's Acceptance:

Approved By	Comment

From: NewTrend Display Technology Co., Ltd.						

## **Document Revision History**

Revision	Date	Description	Changed by
0		Initial Release	
			1
+			
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## **Pin Description:**

Pin No.	Symbol	Function Description	Remark
1~2	LEDK	LED backlight (Cathode).	
3~4	LEDA	LED backlight (Anode).	
5	YU	RTP (No connection)	
6	XR	RTP (No connection)	
7	NC	No connection	
8	RESET	Reset signal input terminal, active at 'L'	
9	CS	Chip select signal input terminal, Active at 'L'	
10	SCK	Write signal input terminal, Active at 'L'. Synchronizing clock signal in SPI mode.	
11	SDA	SPI interface input pin.	
12~19	B0~B7	Blue Data bus	
20~27	G0~G7	Green Data bus	
28~35	R0~R7	Red Data bus	
36	HSYNC	Line synchronizing signal for RGB interface operation.	
37	VSYNC	Frame synchronizing signal for RGB interface operation.	
38	DCLK	Dot clock signal for RGB interface operation.	
39~40	NC	No connection	
41~42	VDD	Power supply for voltage	
43	YD	RTP (No connection)	
44	XL	RTP (No connection)	
45~47	NC	No connection	
48	NC	No connection	
49	NC	No connection	
50	NC	No connection	
51	NC	No connection	
52	DE	Data ENEABLE signal for RGB interface operation.	
53~54	GND	Power ground	

#### **DC Electrical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	Тор	Absolute Max	-20	-	+70	$^{\circ}$
Storage Temperature Range	Тѕт	Absolute Max	-30	-	+80	$^{\circ}$
Digital Supply Voltage	VDD	-	3.0	3.3	3.6	V
logic-High Input voltage	ViH	-	0.7*VDD	-	VDD	٧
logic-Low Input voltage	VIL	-	GND	-	0.3*VDD	٧
logic-High Output Voltage	Vон	-	VDD-0.4	-	VDD	\ \
Logic-Low Output Voltage	Vol	-	GND	-	GND+0.4	V

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Backlight Supply Voltage	Vf	Top=25°C If=40mA	14.5	16	17.5	V
Backlight Supply Current	If		-	40		mA
Backlight Lifetime	-	Top=25°C If=40mA	-	50000		Hrs

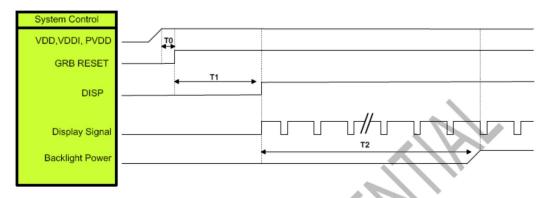
<sup>\*</sup>Backlight lifetime is rated as Hours until half-brightness, under normal operating conditions. The LED of the backlight is driven by current drain, drive voltage is for reference only. Drive voltage must be selected to ensure backlight current drain is below MAX level stated.

## **Optical Characteristics**

Item	Item		Condition	Min.	Тур.	Max.	Unit	
	Тор	-			80	-		
Operating	Bottom	-	00>40	OD>40		80	-	Dog
Viewing Angles	Left	-	CR≥10		80	-	Deg	
	Right	-			80	-		
Contrast Ratio		CR	Center	640	800	-	-	
Luminance		Lv		400	500		cd/m <sup>2</sup>	
Response Time		Tr+Tf			30	40	ms	
	Red	XR	-		0.646 0.332 0.323		-	
	Red	YR						
	Green	Xg	-				ı	
Chromaticity	Green	Yg		TYP-	0.567	TYP+0.05		
Cironaticity	Blue	Хв	-	0.05	0.05 0.134	111-10.03	-	
	Diue	YB			0.121			
	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Xw	-		0.317		-	
	White	Yw	-		0.339		-	

Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.

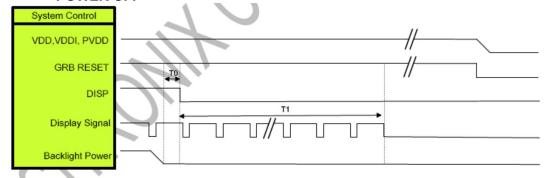
# POWER ON/OFF SEQUENCE POWER ON



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

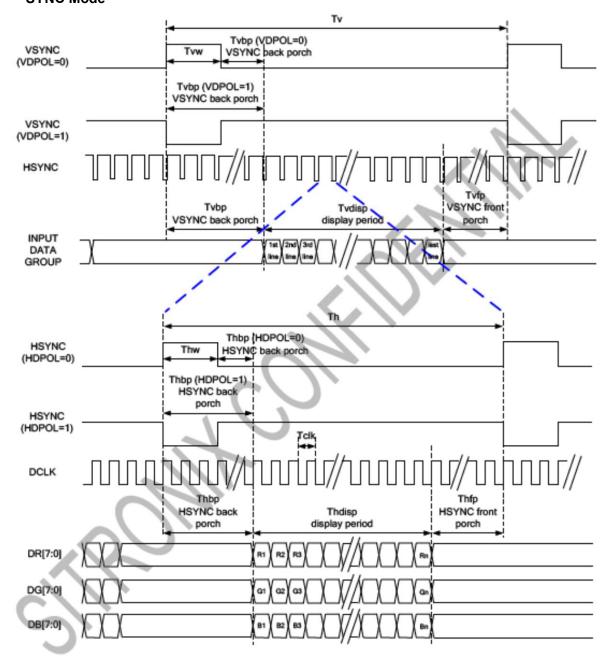
## **POWER OFF**



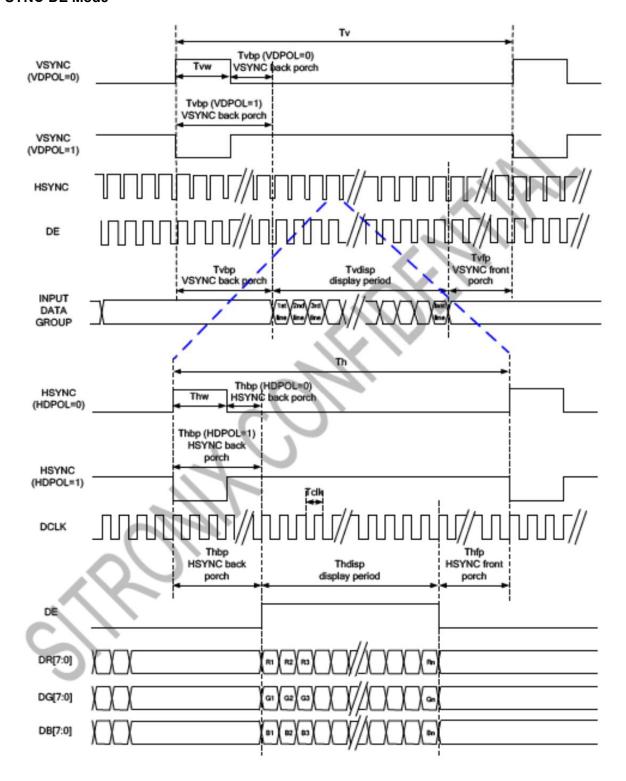
Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

Note: Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

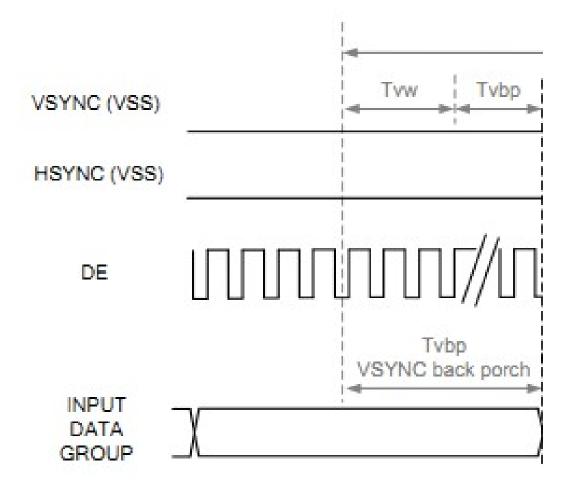
# RGB Interface SYNC Mode



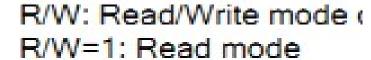
#### **SYNC-DE Mode**



## **DE Mode**

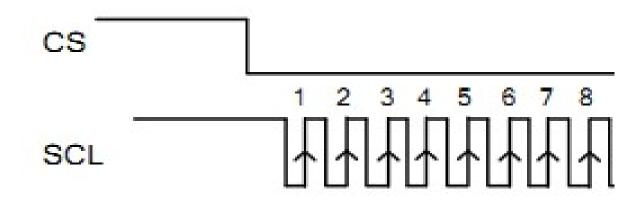






R/W=0: Write mode





Parallel 24-bit RGB Input Timing Table

# Parallel 24-bit RGB Input Timing (PVDD=VDD=V

		Parallel 2	4-bit
	Item	Symbol	Mir
DCLK	Frequency	Fclk	5
DC	LK Period	Tclk	12
3	Period Time	Th	32
3	Display Period	Thdisp	
HSYNC	Back Porch	Thbp	3

**Reliability Test Items and Criteria** 

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80℃±2℃ 96H Restore 4H at 25℃,Power off	
2	Low Temperature Storage	-30 ℃±2℃ 96H Restore 4H at 25℃, Power off	1. After testing,
3	High Temperature Operation	70℃±2℃ 96H Restore 4H at 25℃,Power on	cosmetic and electrical defects
4	Low Temperature Operation	-20℃±2℃ 96H Restore 4H at 25℃, Power on	should not happen.  2. Total current consumption should not be more than
5	High Temperature/Humidity Storage	50℃±2℃ 90%RH 96H Power off	twice of initial value.
6	Temperature Cycle	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$	

#### **Precautions for Use of LCD Modules**

- 1. Handling Precautions
- 1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

<ul> <li>Isopropyl alcohol</li> </ul>	<ul> <li>Ethyl alcoho</li> </ul>
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Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water Ketone Aromatic solvents
- 1.6 Do not attempt to disassemble the LCD Module.
- 1.7 If the logic circuit power is off, do not apply the input signals.
- 1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - a. Be sure to ground the body when handling the LCD Modules.
  - b. Tools required for assembly, such as soldering irons, must be properly ground.
- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

#### 2. Storage precautions

2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :  $10^{\circ}$ C ~  $40^{\circ}$ C

Relatively humidity: ≤60%

2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

3. The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.